DESCRIPTION

The AG2113 is a high voltage, high speed power MOSFET and IGBT driver with independent high and low side referenced output channels based on P_SUB P_EPI process. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high side configuration which operates up to 600V. Logic inputs are compatible with standard CMOS or LSTTL output, down to 3.3V logic. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. Propagation delays are matched to simplify use in high frequency applications.

AG2113 is available in a SOP16 package.

FEATURES

- Fully operational to +600V
- 3.3V logic compatible
- Floating channel designed for bootstrap operation
- Gate drive supply range from 10V to 20V
- UVLO for both channels
- Output Source / Sink Current Capability 3.0A /3.5A
- Separate logic supply range from 5.0V to 20V
- -6V negative Vs ability
- Matched propagation delay for both channels
- Available in a SOP16 package.

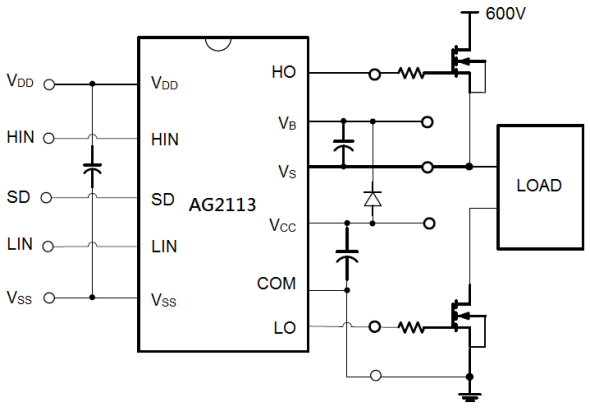
ORDERING INFORMATION

Package Type	Part Number	
SOP16 (wide body) SPQ: 1,500pcs/Reel	M16	AG2113M16R
		AG2113M16VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products		

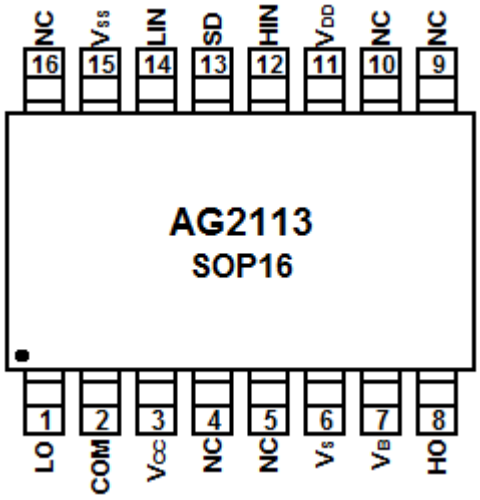
APPLICATION

- High and medium-power motor driver
- Power MOSFET or IGBT driver
- Lighting ballast

TYPICAL APPLICATION CIRCUIT



PIN DESCRIPTION



Top View

Pin #	Symbol	Function
1	LO	Low side gate drive output, in phase with LIN
2	COM	Low side return
3	V_{CC}	Low side supply
4,5,9,10,16	NC	
6	V_S	High side floating supply return
7	V_B	High side floating supply
8	HO	High side gate drive output, in phase with HIN
11	V_{DD}	Logic supply
12	HIN	Logic input for high side gate driver output (HO) , in phase
13	SD	Logic input for shutdown
14	LIN	Logic input for low side gate driver output (LO), in phase
15	V_{SS}	Logic ground

ABSOLUTE MAXIMUM RATINGS

V_B, High Side Floating Supply		-0.3V ~ 625V
V_S, High Side Floating Supply Return		V_B -25V ~ V_B +0.3V
V_{HO}, High Side Gate Drive Output		V_S -0.3V ~ V_B +0.3V
V_{CC}, Low Side and Main Power Supply		-0.3V ~ 25V
V_{LO}, Low Side Gate Drive Output		-0.3V ~ V_{CC} +0.3V
V_{DD}, Logic supply		-0.3V ~ V_{CC} +0.3V
V_{SS}, Logic ground		V_{CC} -25V ~ V_{CC} +0.3V
V_{IN}, Logic input		V_{SS} -0.3V ~ V_{DD} +0.3V
dV_S/dt, Allowable Offset Supply Voltage Transient		50V/ns
ESD, HBM Model		2.5kV
ESD, Machine Model		200V
P_D, Package Power Dissipation @ T_A ≤25°C	SOP16	1.25W
Rth_{JA}, Thermal Resistance Junction to Ambient	SOP16	100℃/W
T_J, Junction Temperature		150℃
T_S, Storage Temperature		-55℃~150℃
T_L, Lead Temperature (Soldering, 10 seconds)		300℃

Stress beyond above listed “Absolute Maximum Ratings” may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Min.	Max.	Units
High Side Floating Supply	V_B	V_S +10	V_S +20	V
High Side Floating Supply Return	V_S	COM-6	600	V
High Side Gate Drive Output Voltage	V_{HO}	V_S	V_B	V
Low Side Supply	V_{CC}	10	20	V
Low Side Gate Drive Output Voltage	V_{LO}	0	V_{CC}	V
Logic Supply	V_{DD}	Vss+3.3	Vss+20	V
Logic Ground	V_{SS}	-5	5	V
Logic Input Voltage(HIN & LIN&SD)	V_{IN}	0	V_{CC}	V
Ambient Temperature	T_A	-40	125	℃

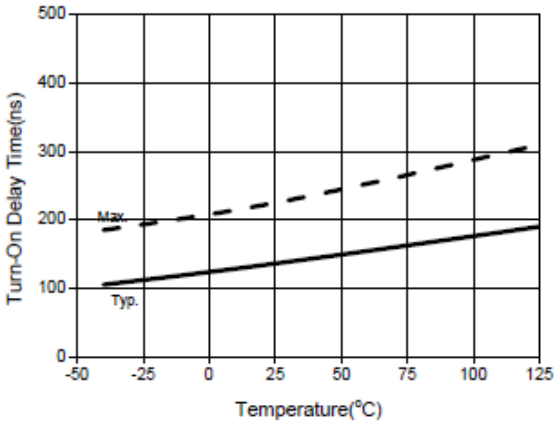
ELECTRICAL CHARACTERISTICS

V_{BIAS} (V_{CC}, V_{BS}) = 15V, C_L = 1000pF and T_A = 25°C, unless otherwise specified.

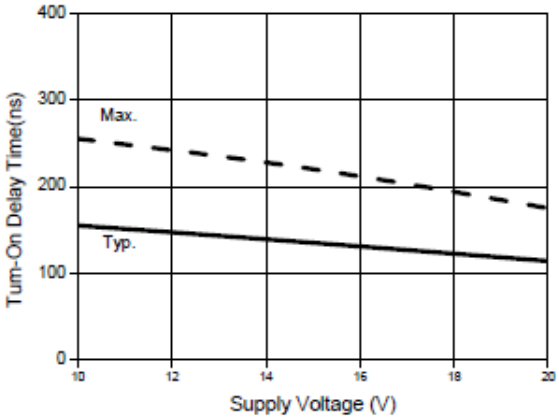
Parameter	Symbol	Conditions	Min	Typ.	Max	Units
Dynamic						
Turn-On Propagation Delay	t_{on}		-	135	220	ns
Turn-Off Propagation Delay	t_{off}		-	130	220	
Shutdown Propagation Delay	t_{sd}		-	135	220	
Delay Matching	MT		-	-	30	
Turn-On Rise Time	t_r		-	20	30	
Turn-Off Fall Time	t_f		-	15	25	
Static						
Logic “1”(IN) Input Voltage	V_{IH}		9.5	-	-	V
Logic “0” (IN) Input Voltage	V_{IL}		-	-	5	
High Level Output Voltage, V_{BIAS} - V_O	V_{OH}		-	-	1.4	
Low Level Output Voltage, V_O	V_{OL}		-	-	0.15	
Quiescent V_{DD} Supply Current	I_{QDD}		-	-	30	μA
Quiescent V_{CC} Supply Current	I_{QCC}		-	120	240	
Quiescent V_B Supply Current	I_{QBS}		-	75	150	
Leakage Current From V_S(600V) to GND	I_{LK}		-	-	10	
Logic “1” Input Bias Current	I_{IN}+		-	20	40	
Logic “0” Input Bias Current	I_{IN}-		-	-	5	
V_{BS} Supply UVLO Threshold	V_{BSU}+		7.5	8.4	9.7	V
	V_{BSU}-		7	8	9.4	
V_{CC} Supply UVLO Threshold	V_{CCU}+		7.4	8.4	9.6	
	V_{CCU}-		7	8	9.4	
Output High Short Circuit Pulsed Current	I_O+		-	3.0	-	A
Output Low Short Circuit Pulsed Current	I_O-		-	4.0	-	

TYPICAL PERFORMANCE CHARACTERISTICS

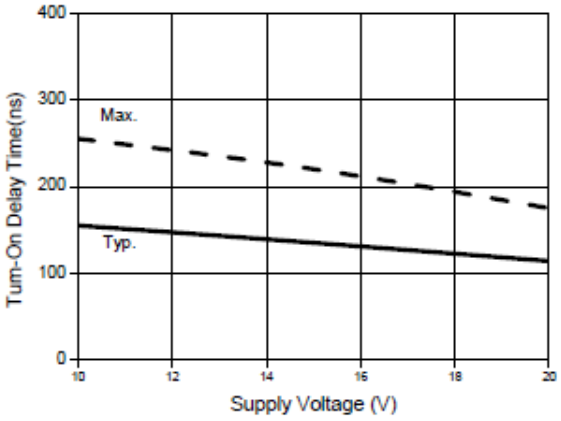
1. Turn-On Delay vs. Temperature



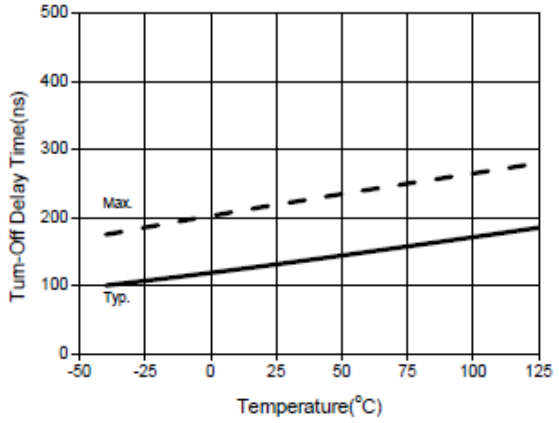
2. Turn-On Delay vs. Supply Voltage



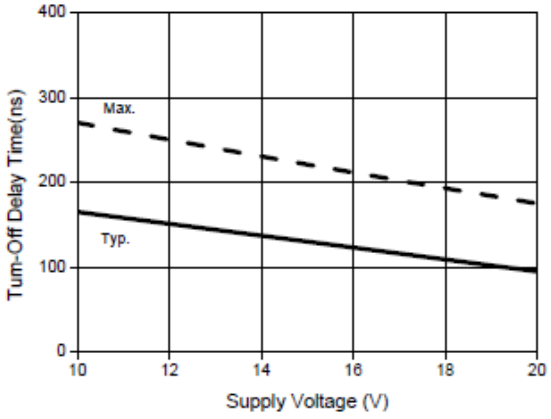
3. Turn-On Delay Time vs. V_{DD} Supply Voltage



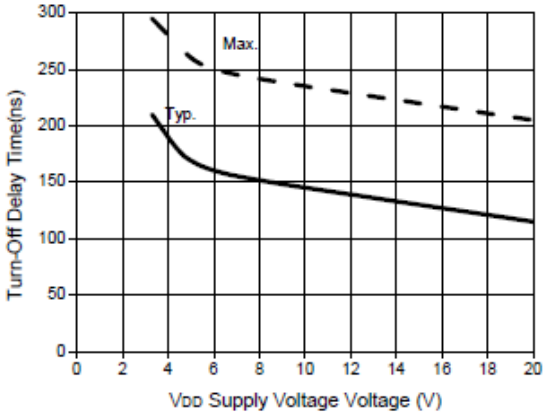
4. Turn-Off Delay Time vs. Temperature



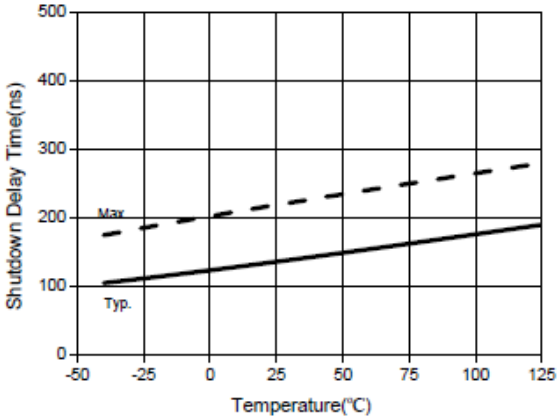
5. Turn-Off Delay Time vs. Supply Voltage



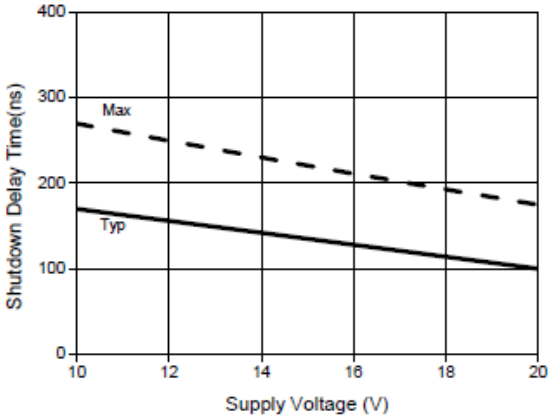
6. Turn-Off Delay Time vs. V_{DD} Supply Voltage



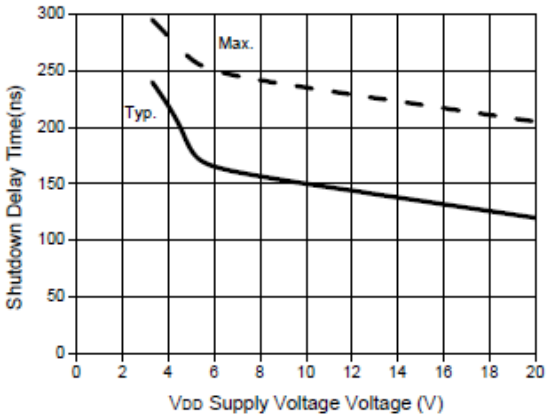
7. Shutdown Delay Time vs. Temperature



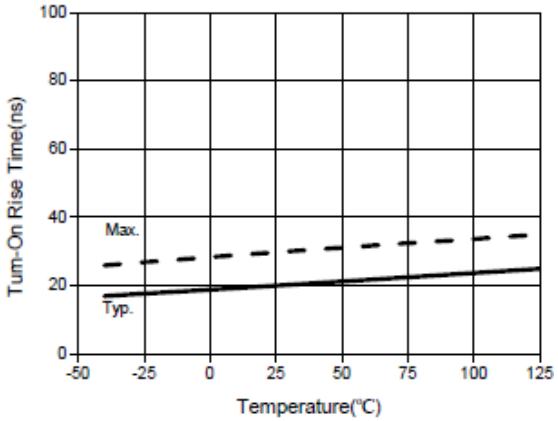
8. Shutdown Delay Time vs. Supply Voltage



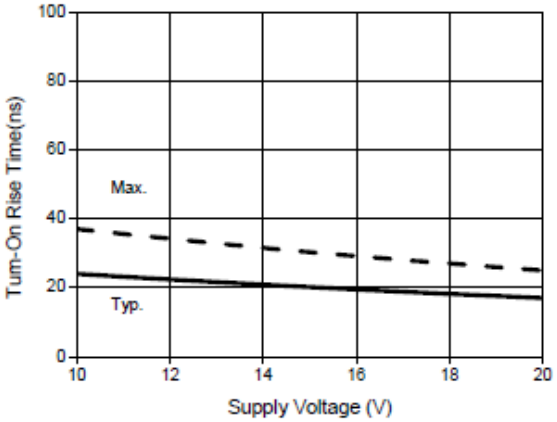
9. Shutdown Delay Time vs. V_{DD} Supply Voltage



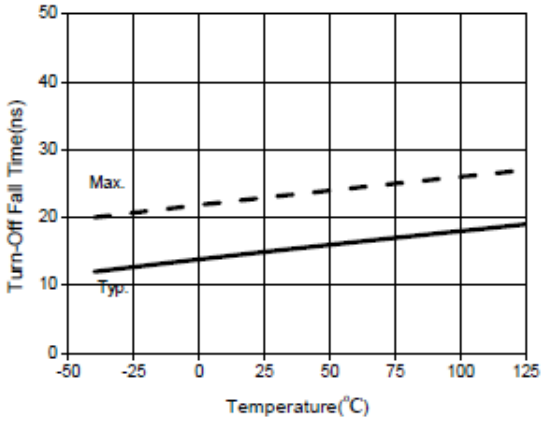
10. Turn-On Rise Time vs. Temperature



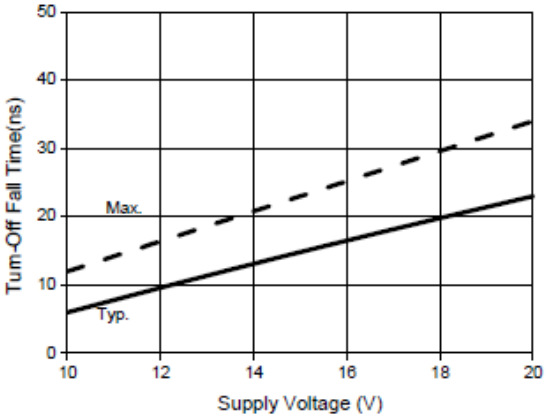
11. Turn-On Rise Time vs. Voltage



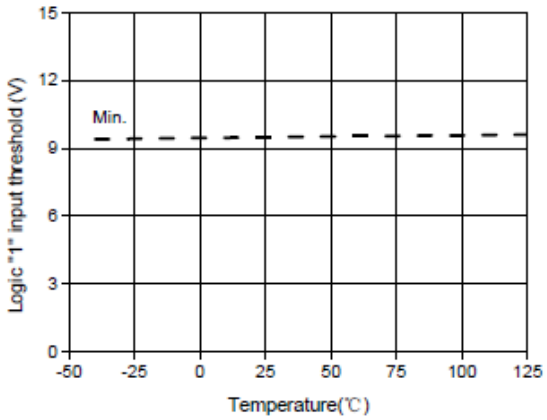
12. Turn-Off Fall Time vs. Temperature



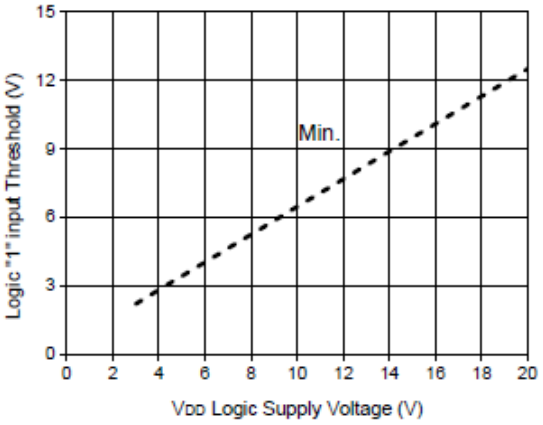
13. Turn-Off Fall Time vs. Voltage



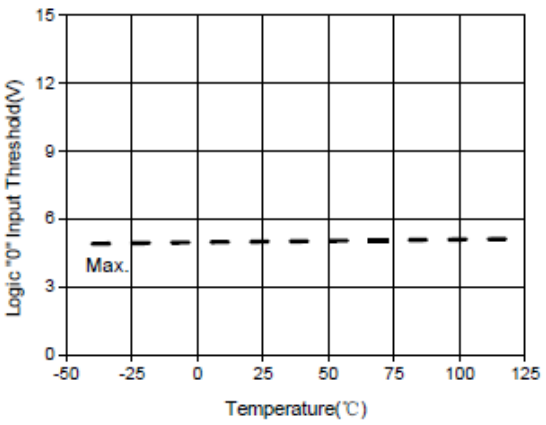
14. Logic "1" Input Voltage vs. Temperature



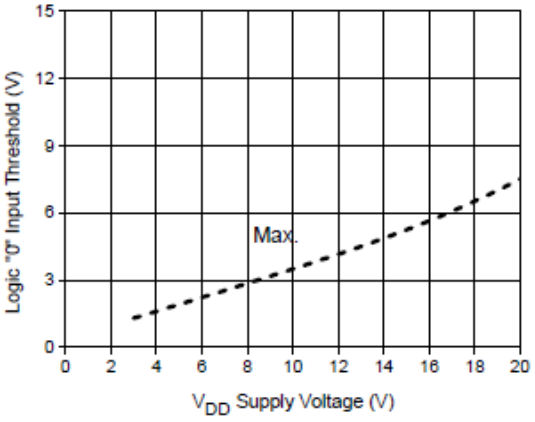
15. Logic "1" Input Voltage vs. Voltage



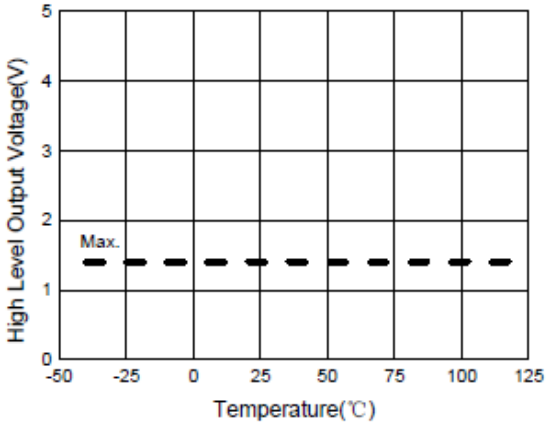
16. Logic "0" Input Voltage vs. Temperature



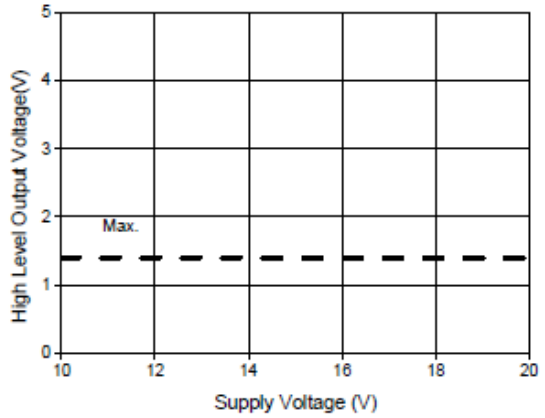
17. Logic "0" Input Voltage vs. Voltage



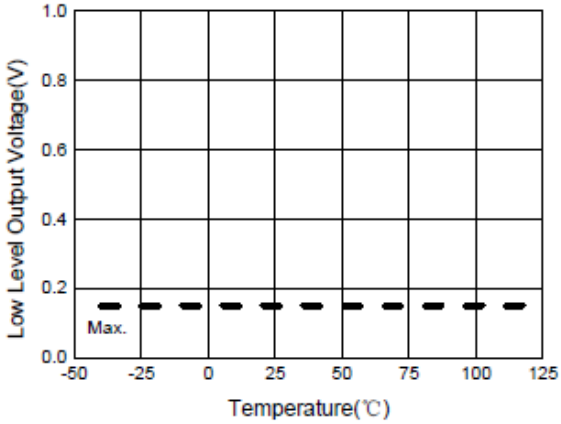
18. High Level Output vs. Temperature



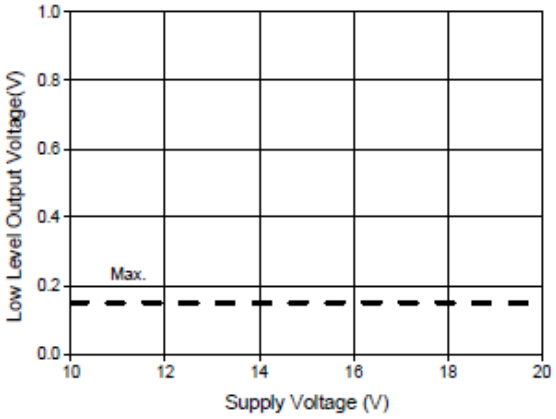
19. High Level Output vs. Voltage



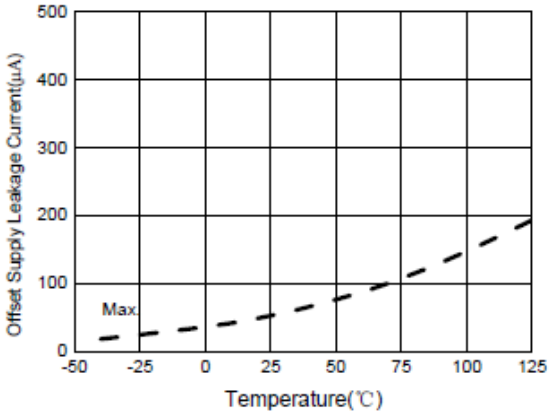
20. Low Level Output vs. Temperature



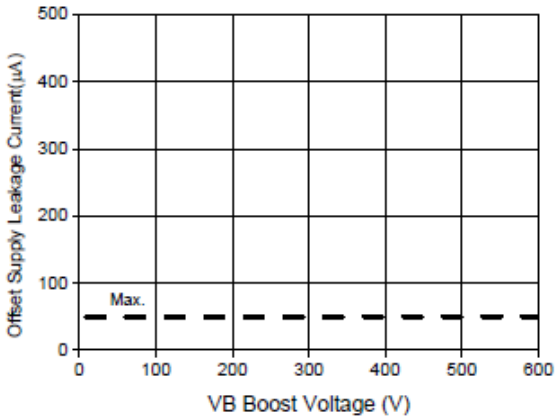
21. Low Level Output vs. Voltage



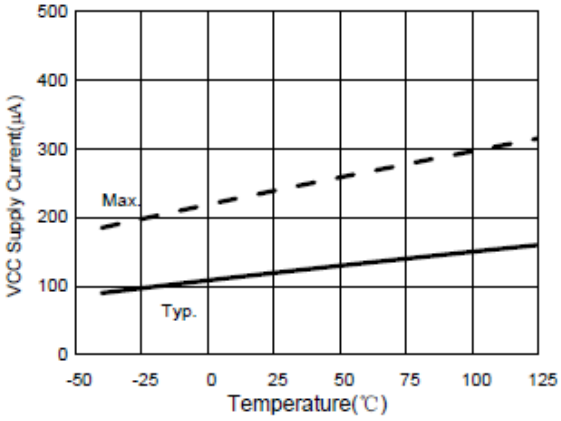
22. Offset Supply Current vs. Temperature



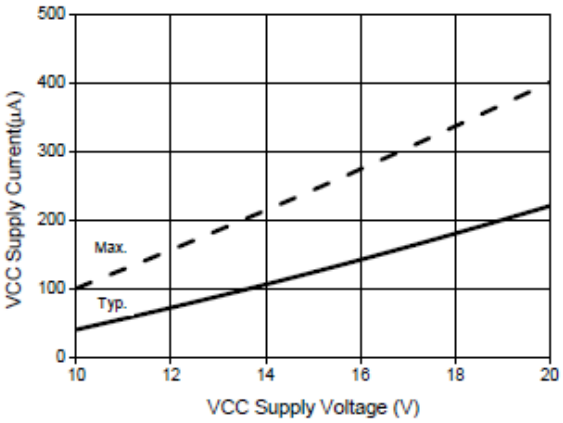
23. Offset Supply Current vs. Voltage



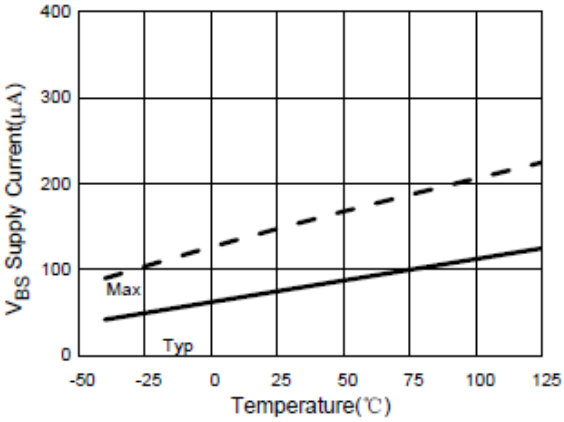
24. V_{CC} Supply Current vs. Temperature



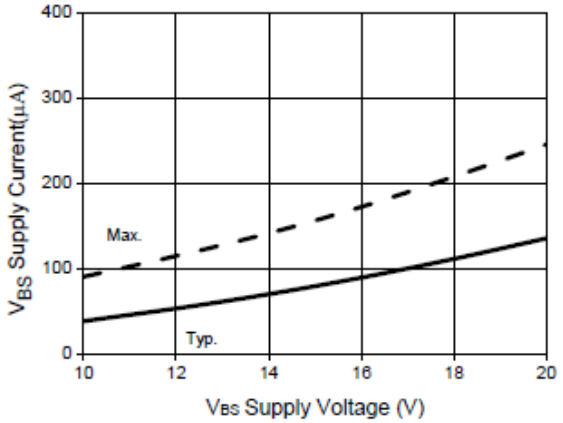
25. V_{CC} Supply Current vs. Voltage



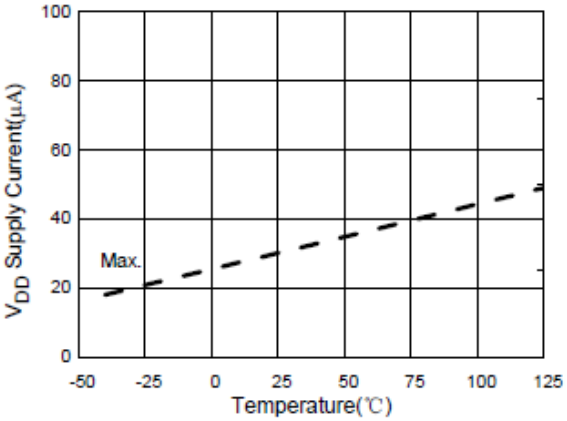
26. V_{BS} Supply Current vs. Temperature



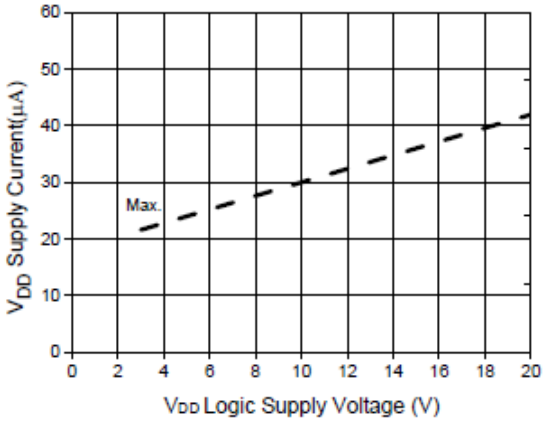
27. V_{BS} Supply Current vs. Voltage



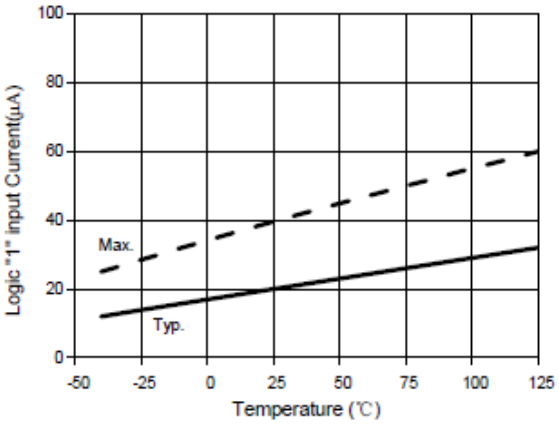
28. V_{DD} Supply Current vs. Temperature



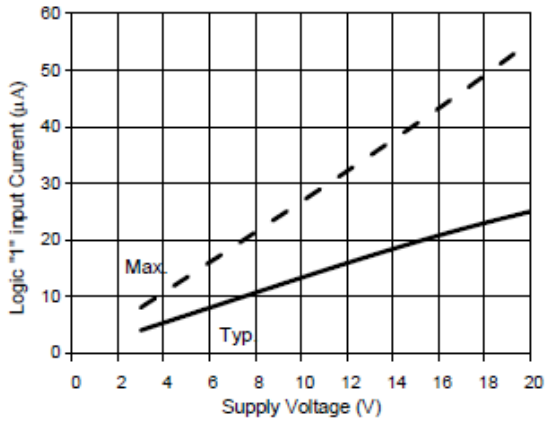
29. V_{DD} Supply Current vs. Voltage



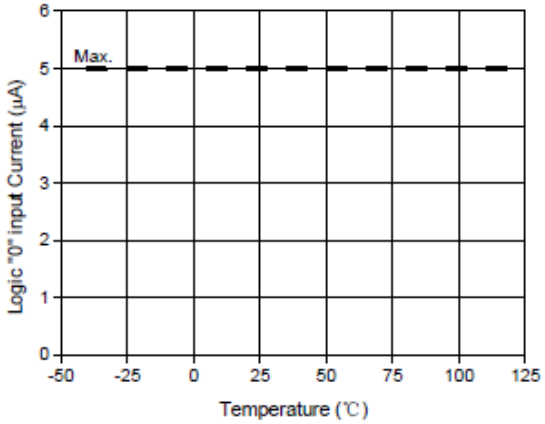
30. Logic "1" Input Current vs. Temperature



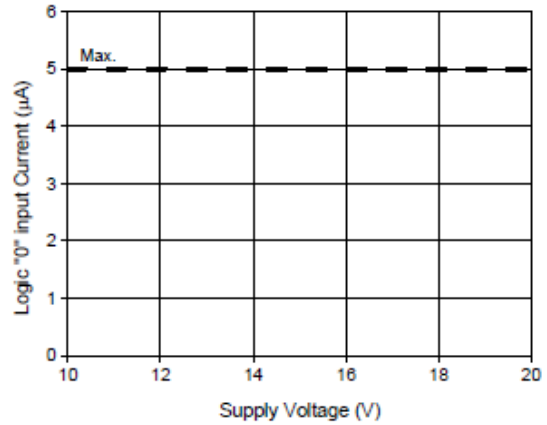
31. Logic "1" Input Current vs. Voltage



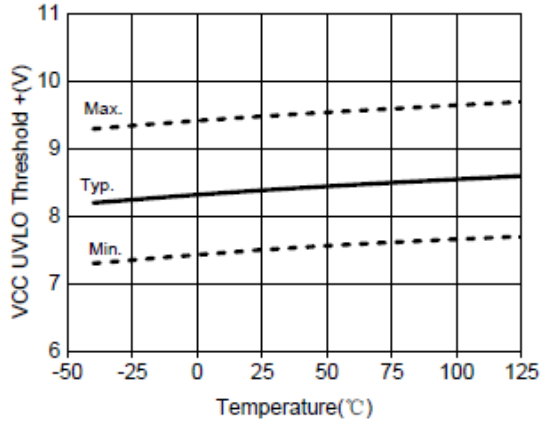
32. Logic "0" Input Current vs. Temperature



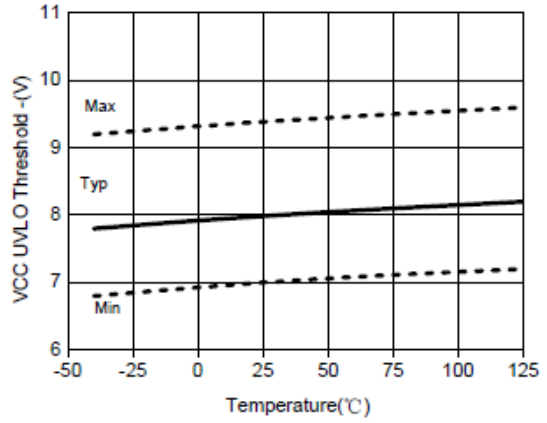
33. Logic "0" Input Current vs. Voltage



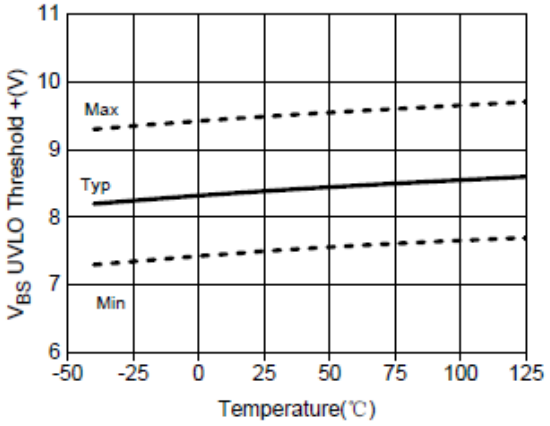
34. V_{CC} Under voltage (+) vs. Temperature



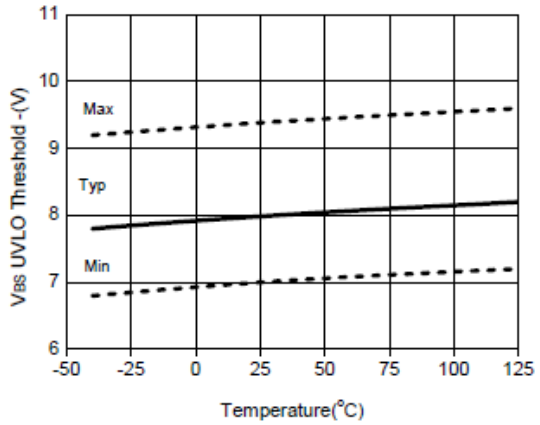
35. V_{CC} Under voltage (-) vs. Temperature



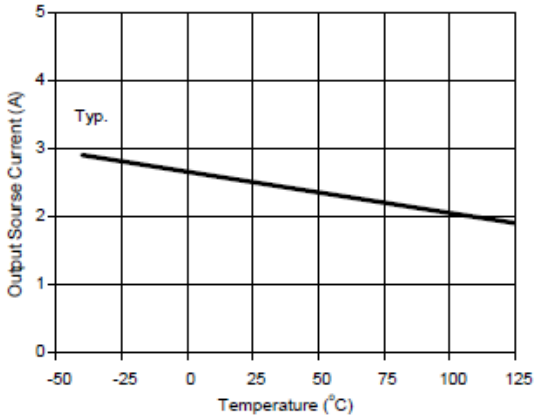
36. V_{BS} Under voltage (+) vs. Temperature



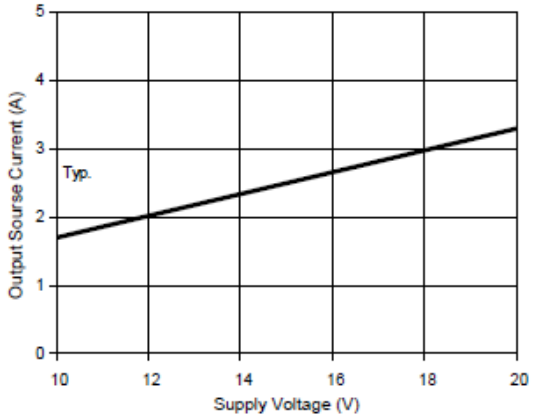
37. V_{BS} Under voltage (-) vs. Temperature



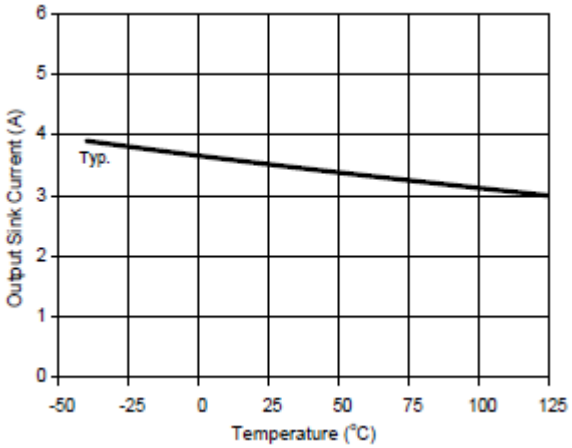
38. Output Source Current vs. Temperature



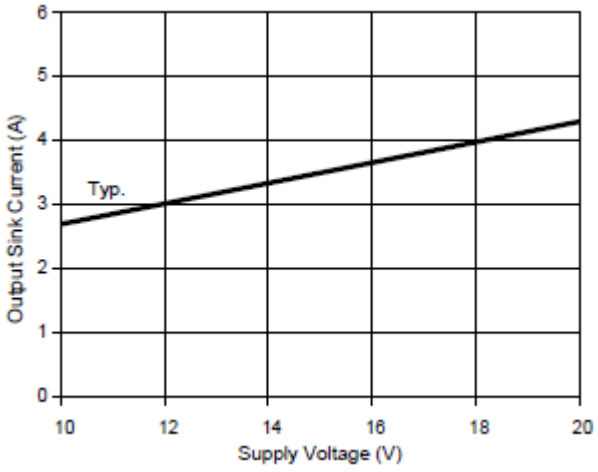
39. Output Source Current vs. Voltage



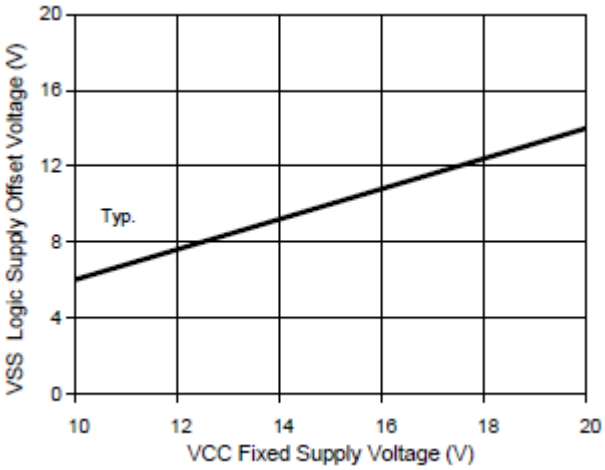
40. Output Sink Current vs. Temperature



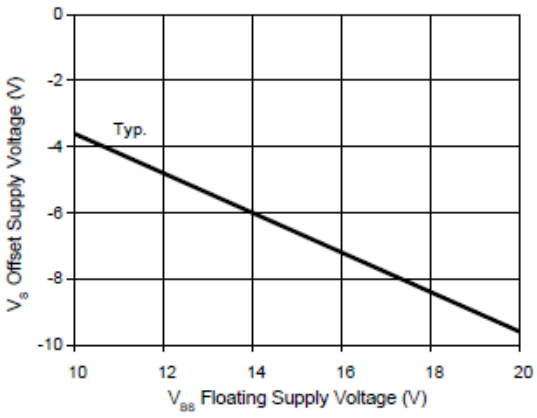
41. Output Sink Current vs. Voltage



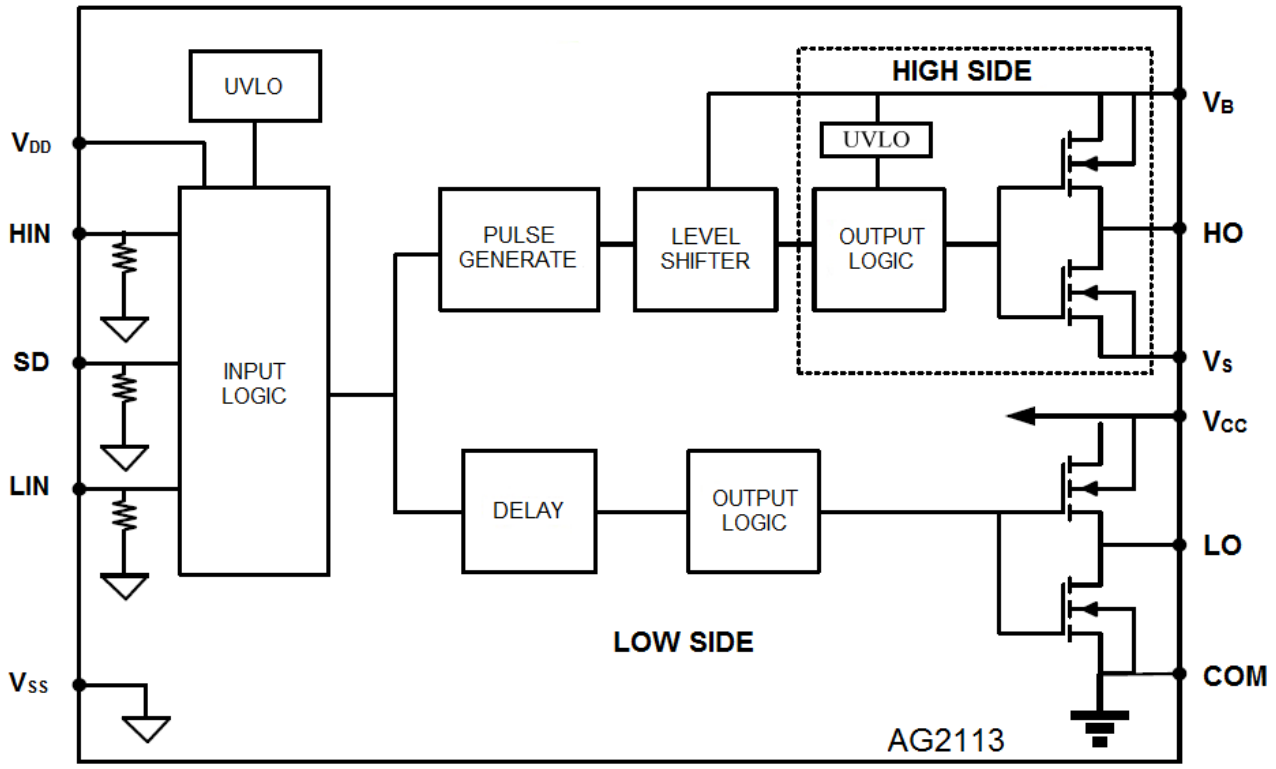
42. Maximum V_{SS} Positive Offset vs. V_{CC} Supply Voltage



43. Maximum V_{SS} Positive Offset vs. V_{CC} Supply Voltage

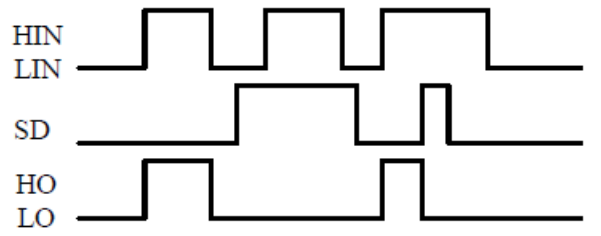


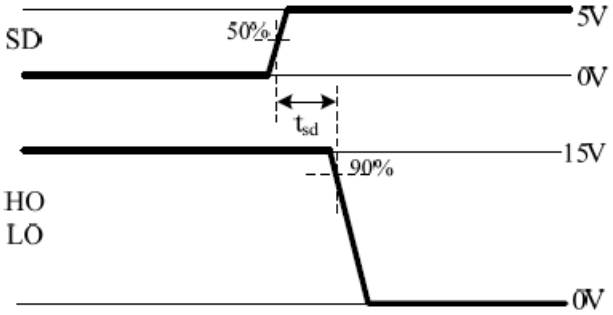
BLOCK DIAGRAM

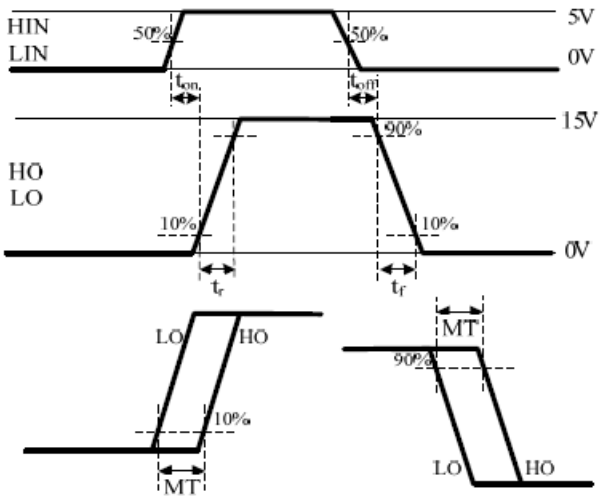


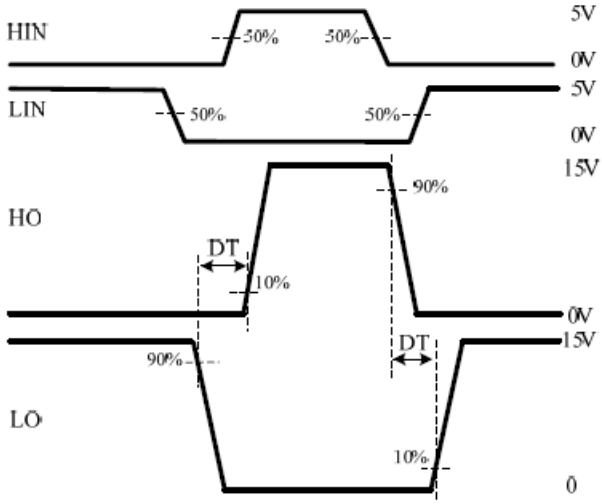
DETAILED INFORMATION

Logic Function & Timing Spec



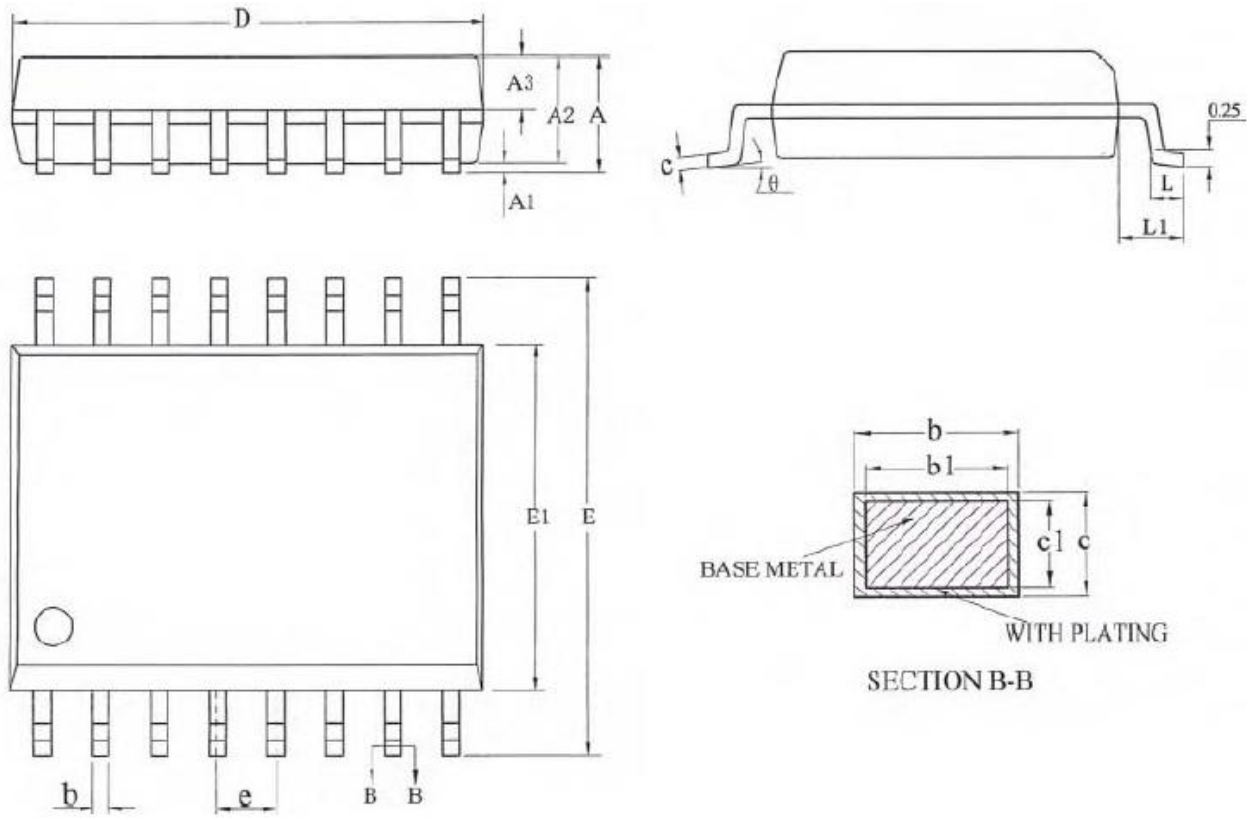






PACKAGE INFORMATION

Dimension in SOP16 (Unit: mm)



Symbol	Min.	Max.
A	-	2.65
A1	0.10	0.30
A2	2.25	2.35
A3	0.97	1.07
b	0.35	0.44
b1	0.34	0.39
c	0.25	0.31
c1	0.24	0.26
D	10.10	10.50
E	10.26	10.60
E1	7.30	7.70
e	1.27 BSC	
L	0.55	0.85
L1	1.4 BSC	
θ	0°	8°

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